

REMARKS

Applicants have carefully reviewed the Office Action dated March 7, 2007. Applicants have amended Claims 1 and 16 to more clearly point out the present inventive concept. Reconsideration and favorable action is respectfully requested.

The claims have been rejected under 35 U.S.C. § 103 in view of the combination of *Bacon, Ware et al.* and *Nolan et al.* The claims have been amended to incorporate new language, as Applicants do not believe that this case is ready for appeal in view of the recent KSR case. The Examiner has not had the opportunity to review the claims in view of the KSR case. Applicants are filing this response in order to afford the Examiner the opportunity to do such. In addition, Applicants have amended the claims to clarify the fact that there are two protocols that the processor must deal with, one being the native protocol and one being the serial data protocol. Each of these protocols operates on a different time base. The first time base is provided by the free running oscillator and the second time base is provided by second time base. Applicants believe that none of the references have shown the content of single chip processor operating without a crystal base reference that is solely contained within the chip and, as such, the claims have been amended to more clearly point out the fact that these two time bases are different and that the processor must accommodate such.

Applicants have now made an earnest attempt in order to place this case in condition for allowance. For the reasons stated above, Applicants respectfully request full allowance of the claims as amended. Please charge any additional fees or deficiencies in fees or credit any overpayment to Deposit Account No. 20-0780/CYGL-26,370 of HOWISON & ARNOTT, L.L.P.

Respectfully submitted,
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AMENDMENT AND RESPONSE
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